

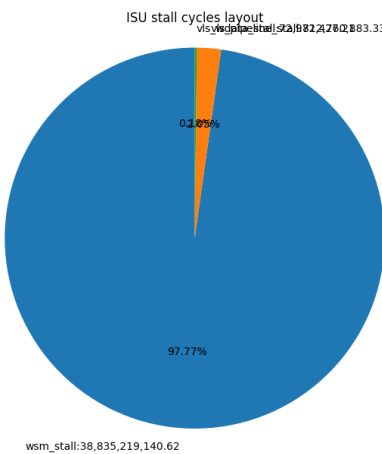
Sub-module: Summary

Name	Description	Value
Total Cycles	cycles use by kernel	10,747,074.94(Kcycles)
AP busy Duty	average AP busy duty of total cycles	15.33%

Sub-module: ISU Statistics

Name: ISU stall cycles layout

Description: ISU stall cycles layout



Sub-module: Memory Statistics

Name	Description	Value
VL1 Hit Rate	hit rate of all instructions in all VL1s	87.34%
L2C Hit Rate	hit rate of all instructions in all L2Cs	94.88%
Global Memory Read bytes	bytes read from global memory	23,652,087,808.0byte
Global Memory Write bytes	bytes write from global memory	4,664,109,056.0byte

Sub-module: Workgroup Memory

Name	Description	Value
shared memory access efficiency	Proportion of NON-CONFLICT access	74.61%

Sub-module: Occupancy

Name	Description	Value
Achieved waves	number of achieved waves	1,554,177.0
Dispatched waves	number of dispatched waves	1,554,176.0

Sub-module: GPU Throughput Statistics

Name	Description	Value
AP MMA Duty ratio	MMA Duty ratio relative to AP active	18.96%